
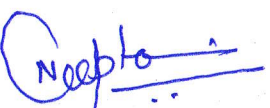



Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)													
Choice Based Credit System (CBCS) Scheme													
M.Tech. in Embedded System (w.e.f. July 2020)													
IV - SEMESTER													
Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*		
1	MTES401	Dissertation Part II/Industrial Internship		0	0	28	0	0	0	300	200	500	14
TOTAL				0	0	28	0	0	0	300	200	500	14
*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.													
** Either Student has to undergo Industrial Internship or opt Dissertation Part II.													


Chairperson
Board of Studies
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 Indore


Chairperson
 Faculty of Studies
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, INDORE


Registrar
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore


Controller of Examinations
 Shri Vaishnav Vidyapeeth Vishwavidyalaya


Vice Chancellor
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore